

**Notice of Allowability**

Application No.

10/018,440

Applicant(s)

TAKADA ET AL.

Examiner

Art Unit

Samson B. Lemma

2132

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 11/10/2005.
2. ☒ The allowed claim(s) is/are 1-30.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____.   |

### ***DETAILED ACTION***

1. This is in reply to amendment filed on November 10, 2005. **Claims 27-30 have been amended.** Claims 1-26 have been allowed on the previous office action. Claims 1-30 have been examined.
2. **No new claim has been** added.

### ***Allowable Subject Matter***

3. **Claims 1-30** are allowed.
4. The following is an examiner's statement of reasons for allowance:
5. **Claims 1-26 had been allowed previously** and the following reason for allowance has been recited in the previous office action. The reason for allowance indicated before which is shown below is also valid now. With respect to the independent claims **1, 13, 16, 19, 23-25.**

The following references disclose the general subject matter of the invention.

1. US 6742715/WO 9852160
2. US 6005942
3. US 6003113

**The above three closest references cited in the record** have been considered.

The subject matter disclosed in all the three cited references closely discloses some of the limitation recited in the independent claims of the application.

For instance, **the first closest reference on the record**, namely, US 6742715, discloses how a single integrated circuit supports multiple applications. It further discloses method of flexibly loading an application and its associated data from an application provider onto an IC card. The application and its associated data are divided into segments which can each fit into the input buffer of an Integrated circuit card.

Each segment is transmitted separately and the Integrated circuit card then stores the segment in an available space in the IC card's memory. Other applications may use part of a prior application space leaving empty segments. The new application is loaded into a number of these segments.

**The 2<sup>nd</sup> closest reference, US 6005942**, provided with IDS discloses multi application smart card method for facilitating a post issuance downloading of an application on to the smart card. The reference discloses a method for card issuers to securely add applications during the lifetime of the card after the card has already been issued (post issuance). Loading of an application and/or objects from an application server via a card acceptance device (and its supporting system infrastructure delivery mechanism) onto a card post issuance is performed in a secure and confidential manner. A smart card includes a card domain application that manages the card. Any number of security domain applications on the card provide security for loaded applications by managing keys; each application is associated with a security domain. Each of the card domain and security domains has a command interface for off-card communication, and an API for internal card use. The card life cycle includes the states of masked, initialized, load secured and blocked. An application life cycle includes the states of not available, loaded, installed, registered, personalized, activated and blocked. An application can block the card. The above reference meets most of the recitation of the independent claims.

**The 3<sup>rd</sup> closest reference namely, US 6003113**, discloses how a multi-purpose integrated-circuit card is used for storing electronic money and other information which provides increased security to avoid data to be managed by only a single program and provides access by another program. A medical information program is provided as an example.

However, the references which are cited above does not disclose or suggest the

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following particular limitation of the respective claim 1.

A data processing method for processing so that a portable device mounting an integrated circuit storing memory area division data and first area management key data, is authorized to perform at least one of a write operation to a memory area of said integrated circuit and a rewrite operation to the memory area on the condition that the first area management key data makes a second service provider provide a service using part of said memory area of said integrated circuit when said portable device is issued by a first service provider providing a service using said memory area,

said data processing method comprising the steps of:

having a memory area operation unit managing said key data for memory area division data encrypt first module data including second area management key data by the key memory area division data, and providing the same to the first service provider;

having the issuer of the portable device which is said first service provider, encrypt second module data including the encrypted first module by using said first area management key data providing the same to said memory area operation unit; and,

under the control of the memory area operation unit, providing the encrypted second module data to the integrated circuit, decrypting the second module data by using the first area management key data in the integrated circuit, decrypting the first module data in the decrypted second module data by using the key memory division data, and dividing the memory area to a first memory area to be used for the services of the first service provider and a second memory area to be used the services of the second service provider by using the second area management key data obtained from the decryption of the data.

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**None of the prior art of record taken singularly or in combination teaches or suggests an integrated circuit with all the limitations recited in respective claim 1. For this reason, the independent claim 1 is allowed. All the rest of the independent claims include similar limitation as that of claim 1 and are allowed for the same reason provided above.**

6. The dependent claims which are dependent on the above independent claims 1, 13, 16, 19 and 23-25, being further limiting to the independent claims, definite and enabled by the specification are also allowed.

7. **With respect to the independent claims 27 and 29, claims 27 and 29 had been rejected under 35 U.S.C § 101 in the previous office action.**

Applicant has successfully amended **both claim 27 and 29 and overcome the 101 rejection set forth in the previous office action.**

8. **With respect to the independent claims 28 and 30, claims 28 and 30 had been rejected under 35 U.S.C § 112 in the previous office action.**

Applicant has successfully **amended both claim 28 and 30 and overcome the 112 rejection set forth in the previous office action.**

As far as the subject matter **of the independent claims 26-30 are concerned**, all the claims recites similar limitation as that of the independent claim 1, therefore these claims are allowed for the same reason provided for independent claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submission should be clearly labeled "Comments on Statement of Reasons for Allowance."

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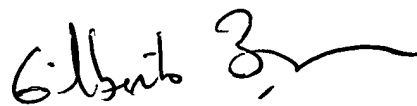
### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Samson B Lemma whose telephone number is 571-272-3806. The examiner can normally be reached on Monday-Friday (8:00 am---4: 30 pm). If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BARRON JR GILBERTO can be reached on 571-272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SAMSON LEMMA

**S.L.**  
11/20/2005

  
GILBERTO BARRON JR.  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100